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# Specifications

**TFT-LCDmodule**

**Model No: QY14002B0-00**

**Customer name:**

**The project name:**

| For Customer's Acceptance |         |
|---------------------------|---------|
| Approved by               | Comment |
|                           |         |

|             | Signature | Date |
|-------------|-----------|------|
| Prepared by |           |      |
| Checked by  |           |      |
| Approved by |           |      |

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## 1 General Description

**QY14002B0-00** is a transmissive type a-Si TFT-LCD (amorphous silicon thin film transistor liquid crystal display) module, which is composed of a TFT-LCD panel, a driver circuit a backlight unit, The panel size is 1.44inch and thresolution is 128x128. High image quality a-Si TFT LCD module. Partial-screen display function is available. Sleep and Stand-by modes are available for power saving.

### 1.1 Features

| No | Item              | Specification               | Remark |
|----|-------------------|-----------------------------|--------|
| 1  | Display Mode      | High Resolution & Wide View |        |
| 2  | Screen Size       | 1.44inch (diagonal)         |        |
| 3  | Resolution        | 128XRGBX128                 |        |
| 4  | Color Number      | 65K TFT                     |        |
| 5  | Color Arrangement | RGB-stripe                  |        |
| 6  | Driver IC         | ST7735S                     |        |
| 7  | Back Light        | White LED*2                 |        |
| 8  | Viewing Direction | 12 O'clock                  |        |
| 9  | Interface         | SPI4W                       |        |
| 10 | Surface Treatment | UV Cut                      |        |
| 11 | touch panel       |                             |        |

### 1.2 Application

- ◆ Mobile phone.
- ◆ Portable multimedia device.

## 2 Outline Dimension

The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

| Parameter          | Specifications                                       | Unit              |
|--------------------|------------------------------------------------------|-------------------|
| Outline dimensions | 29.5(W) x36.5(H) x 2.55+-0.1(D) (LCM,no include FPC) | mm                |
| Active area        | 25.5(W) x26.5(H)                                     | mm                |
| Resolution         | 128(H)RGBx128(V) dots                                | -                 |
| Dot size           | 0.1992x0.207                                         | mm                |
| Luminance value    | 220                                                  | cd/m <sup>2</sup> |



|                                              |                             |             |
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## 3 Electrical Characteristics

### 3.1 TFT-LCD Module

| Item                            | Symbol | Unit | Condition              | Min.     | Typ. | Max.     | Note      |
|---------------------------------|--------|------|------------------------|----------|------|----------|-----------|
| Power and Operation Voltage     |        |      |                        |          |      |          |           |
| Analog Operating Voltage        | VCI    | V    | Operating voltage      | 2.5      | 2.8  | 3.3      | Note2     |
| Logic Operating Voltage         | VDDI   | V    | I/O supply voltage     | 1.65     | 2.8  | 3.3      | Note2     |
| Digital Operating voltage       | VCORE  | V    | Digital supply voltage | -        | 1.5  | -        | Note2     |
| Gate Driver High Voltage        | VGH    | V    | -                      | 12.0     | -    | 21.0     | Note3     |
| Gate Driver Low Voltage         | VGL    | V    | -                      | -12.5    | -    | -7.0     | Note3     |
| Driver Supply Voltage           | -      | V    | [VGH-VGL]              | -        | -    | 32       | Note3     |
| Input and Output                |        |      |                        |          |      |          |           |
| Logic High Level Input Voltage  | VIH    | V    | -                      | 0.7*VDDI | -    | VDDI     | Note1,2,3 |
| Logic Low Level Input Voltage   | VIL    | V    | -                      | VSS      | -    | 0.3*VDDI | Note1,2,3 |
| Logic High Level Output Voltage | VOH    | V    | IOL=-1.0mA             | 0.8*VDDI | -    | VDDI     | Note1,2,3 |
| Logic Low Level Output Voltage  | VOL    | V    | IOL=1.0mA              | VSS      | -    | 0.2*VDDI | Note1,2,3 |
| Logic High Level Input Current  | IIH    | uA   | -                      | -        | -    | 1        | Note1,2,3 |
| Logic Low Level input Current   | IIL    | uA   | -                      | -1       | -    | -        | Note1,2,3 |
| Logic Input Leakage Current     | ILEA   | uA   | VIN=VDDI or VSS        | -0.1     | -    | +0.1     | Note1,2,3 |

Note 1: VDDI=1.65 to 3.3V, VCI=2.5 to 3.3V, AGND=VSS=0V, Ta=-30 to 70 (to +85 no damage) °C.

Note2: Please supply digital VDDI voltage equal or less than analog VCI voltage.

### 3.2 Back-Light Unit

| Item            | Symbol | Min.  | Typ. | Max. | Unit  | Remark             |
|-----------------|--------|-------|------|------|-------|--------------------|
| Current         | IF     | 30    | 40   | 50   | mA    | IF=40mA<br>IF=3.2V |
| Forward voltage | VF     | 2.9   | 3.2  | 3.5  | V     |                    |
| Chroma          | X      | 0.250 |      | 0.30 |       |                    |
|                 | Y      | 0.250 |      | 0.30 |       |                    |
| Brightness      | L      | 4000  |      |      | Cd/m2 |                    |
| Uniformity      | UBL    | 80    |      |      | %     |                    |

- 2 LEDs multiple circuit
- The luminous intensity of LED is strongly dependent on the driving current.
- It is recommended the input of backlight to be constant current rather than constant voltage.

|                                              |                             |             |
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## 5 TFT-LCM Interface Specification

| Pin No | Symbol | Description                                                                                                                                                                        | Note |
|--------|--------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------|
| 1      | GND    | Ground                                                                                                                                                                             |      |
| 2      | LEDK   | Power supply Cathode input for backlight                                                                                                                                           |      |
| 3      | LEDA   | Power supply Anode input for backlight                                                                                                                                             |      |
| 4      | NC     | NC                                                                                                                                                                                 |      |
| 5      | RESET  | Reset signal input Pin                                                                                                                                                             |      |
| 6      | A0     | -Display data/command selection pin in MCU interface.<br>DCX='1': display data or parameter.<br>DCX='0': command data.<br>-If not used, please fix this pin at VDDI or DGND level. |      |
| 7      | SDA    | SPI interface input pin.                                                                                                                                                           |      |
| 8      | SCK    | -Write enable in MCU parallel interface.<br>In SPI mode, this is used as SCL.<br>-If not used, please fix this pin at VDDI or DGND level.                                          |      |
| 9      | VCC    | Power supply input for LCM:2.8V                                                                                                                                                    |      |
| 10     | IOVCC  | Power supply input for LCM:1.8V                                                                                                                                                    |      |
| 11     | CS     | -Chip selection pin<br>Low enable.<br>High disable..                                                                                                                               |      |
| 12     | GND    | Ground                                                                                                                                                                             |      |
| 13~16  | NC     | NC                                                                                                                                                                                 |      |
| 17,18  | GND    | Ground                                                                                                                                                                             |      |
|        |        |                                                                                                                                                                                    |      |

|                                              |                             |             |
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## 6 Description of Interface'Signal

### 6.1 RGB Interface Timing Characteristics

Table 24. (24/18/16-bit I/F, IOVCC = 1.65 to 3.30V, VCC = 2.60 to 3.30V)

| Item                                  | Symbol       | Unit | Min | Typ | Max |
|---------------------------------------|--------------|------|-----|-----|-----|
| VSYNC/HSYNC setup time                | tSYNCS       | ns   | 5   | -   | -   |
| VSYNC/HSYNC hold time                 | tSYNCH       | ns   | 5   | -   | -   |
| DE setup time                         | tENS         | ns   | 5   | -   | -   |
| DE hold time                          | tENH         | ns   | 5   | -   | -   |
| PCLK "Low" level pulse width          | PWDL         | ns   | 10  | -   | -   |
| PCLK "High" level pulse width         | PWDH         | ns   | 10  | -   | -   |
| PCLK cycle time                       | tCYCD        | ns   | 20  | -   | -   |
| Data setup time                       | tPDS         | ns   | 6   | -   | -   |
| Date hold time                        | tPDH         | ns   | 6   | -   | -   |
| PCLK, VSYNC, HSYNC, DE rise/fall time | trgbr, trgbf | ns   | -   | -   | 13  |

### 6.2 DC Characteristics

Table 15. DC Characteristics

| Item                                                                          | Symbol           | Unit | Test Condition                           | Min       | Typ | Max       | Notes |
|-------------------------------------------------------------------------------|------------------|------|------------------------------------------|-----------|-----|-----------|-------|
| Input high voltage                                                            | V <sub>IH</sub>  | V    | IOVCC = 1.65~3.3                         | 0.8*IOVCC | -   | IOVCC     |       |
| Input low voltage                                                             | V <sub>IL</sub>  | V    | IOVCC = 1.65~3.3                         | -0.3      | -   | 0.2*IOVCC |       |
| Output high voltage (1)<br>(DB17-0, SDO)                                      | V <sub>OH1</sub> | V    | IOVCC = 1.65~3.3<br>IOH = 0.1mA          | 0.8*IOVCC | -   | -         |       |
| Output low voltage (1)<br>(DB17-0, SDO)                                       | V <sub>OL1</sub> | V    | IOVCC = 1.65~3.3<br>IOL = 0.1mA          | -         | -   | 0.2*IOVCC |       |
| I/O leakage current                                                           | I <sub>LI</sub>  | μA   | Vin = 0~IOVCC                            | -1        | -   | 1         |       |
| Current consumption<br>during standby mode:<br>(IOVCC - GND) +<br>(VCC - GND) | I <sub>ST</sub>  | μA   | IOVCC = VCC = VCI =<br>2.8V<br>Ta = 25°C | -         | 1.4 | 10        |       |

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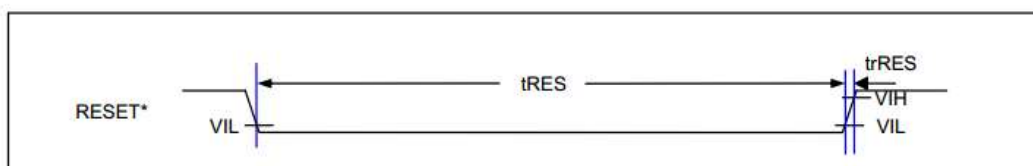
### 6.3 AC Characteristics

| Item                                  |                    | Symbol     | Unit | Min | Typ | Max |
|---------------------------------------|--------------------|------------|------|-----|-----|-----|
| Serial clock cycle time               | Write (received)   | tSCYC      | ns   | 20  | -   | -   |
|                                       | Read (transmitted) |            |      | 100 | -   | -   |
| Serial clock "High" level pulse width | Write (received)   | tSCH       | ns   | 10  | -   | -   |
|                                       | Read (transmitted) |            |      | 50  | -   | -   |
| Serial clock "Low" level pulse width  | Write (received)   | tSCL       | ns   | 10  | -   | -   |
|                                       | Read (transmitted) |            |      | 50  | -   | -   |
| Serial clock rise/fall time           |                    | tscr, tscf | ns   | -   | -   | 20  |
| Chip select setup time                |                    | tCSU       | ns   | 20  | -   | -   |
| Chip select hold time                 |                    | tCH        | ns   | 10  | -   | -   |
| Serial input data setup time          |                    | tSISU      | ns   | 5   | -   | -   |
| Serial input data hold time           |                    | tSIH       | ns   | 10  | -   | -   |
| Serial output data setup time         |                    | tSOD       | ns   | 80  | -   | 150 |
| Serial output data hold time          |                    | tSOH       | ns   | -   | -   | 80  |

### 6.4 Reset Characteristics

Table 23. (Condition: IOVCC = 1.65 to 3.30V, VCC = 2.60 to 3.30V)

| Item                    | Symbol | Unit | Min | Typ | Max |
|-------------------------|--------|------|-----|-----|-----|
| Reset "Low" level width | tRES   | ms   | 1   | -   | -   |
| Reset rise time         | trRES  | μs   | -   | -   | 10  |



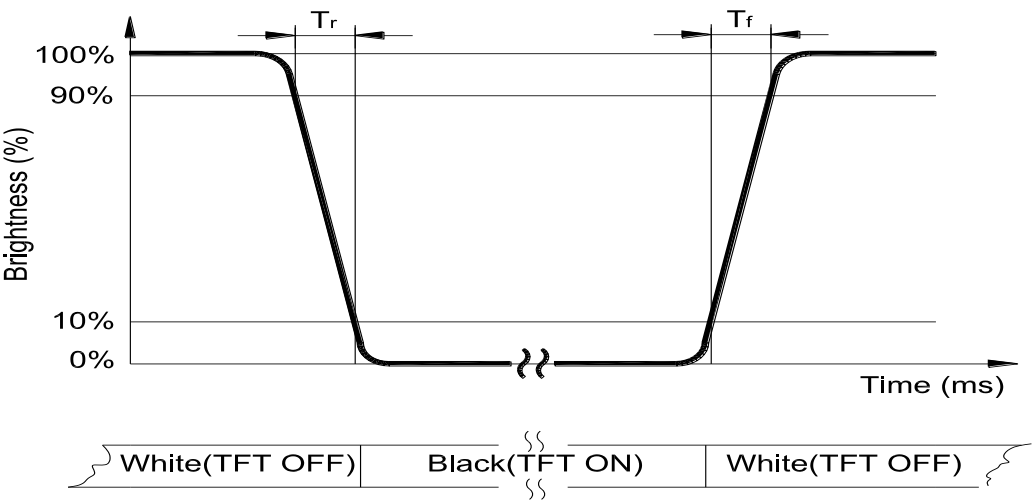
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## 7. Optical Specification

| C. Optical specifications                                                          |        |                    |               |       |       |      |            |
|------------------------------------------------------------------------------------|--------|--------------------|---------------|-------|-------|------|------------|
| Item                                                                               | Symbol | Condition          | Specification |       |       | Unit | Remark     |
|                                                                                    |        |                    | Min.          | Typ.  | Max.  |      |            |
| Response time (By Quick)                                                           | Tr+Tf  | $\theta = 0^\circ$ | -             | 30    |       | ms   | Note 5     |
| Contrast ratio                                                                     | CR     | $\theta = 0^\circ$ | -             | 250   | -     |      | Note 2,6   |
| Viewing angle                                                                      | Top    | $CR \geq 10$       | -             | 45    | -     | deg. | Note 2,6,7 |
|                                                                                    | Bottom | $CR \geq 10$       | -             | 20    | -     |      |            |
|                                                                                    | Left   | $CR \geq 10$       | -             | 45    | -     |      |            |
|                                                                                    | Right  | $CR \geq 10$       | -             | 45    | -     |      |            |
| Color chromaticity<br>( CF only with ITO,<br>light source is C<br>light, CIE 1931) | Wx     | $\theta = 0^\circ$ | -0.02         | 0.308 | +0.02 |      | Note 3     |
|                                                                                    | Wy     |                    |               | 0.325 |       |      |            |
|                                                                                    | Rx     |                    |               | 0.612 |       |      |            |
|                                                                                    | Ry     |                    |               | 0.329 |       |      |            |
|                                                                                    | Gx     |                    |               | 0.299 |       |      |            |
|                                                                                    | Gy     |                    |               | 0.567 |       |      |            |
|                                                                                    | Bx     |                    |               | 0.144 |       |      |            |
|                                                                                    | By     |                    |               | 0.110 |       |      |            |
| NTSC                                                                               |        |                    | -             | 55    | -     | %    | Note 3     |
| Cross talk                                                                         | Ct     |                    | -             | 2     | -     | %    | Note 9     |
| Transmittance                                                                      | Trans  |                    | -             | 5     | -     | %    | Note 4     |



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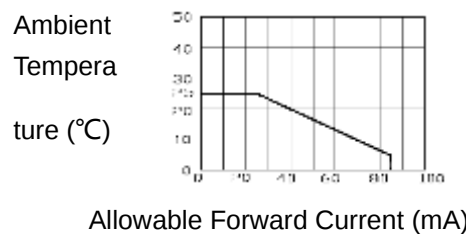


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## 8 Environment Absolute Maximum Ratings

| Item                        | Symbol | Min | Max | Unit | Remark  |
|-----------------------------|--------|-----|-----|------|---------|
| Operation temperature range | Top    | -10 | 60  | °C   | Ambient |
| Storage temperature range   | Tst    | -20 | 70  | °C   | Ambient |

- Corrosive gas environment is not acceptable.
- TFT-LCD color will change slightly depending on environment temperature. This phenomenon is reversible. Current reduction rate of LED backlight is according to the graph indicated below:



## 9 Reliability Test Items

| Item                                            | Test Condition                                                      |                              | Criterion                                                                                                                                                |
|-------------------------------------------------|---------------------------------------------------------------------|------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------|
| High Temperature Storage                        | 70 °C, 240 hrs                                                      |                              | There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition. |
| Low Temperature Storage                         | -20 °C, 240 hrs                                                     |                              |                                                                                                                                                          |
| High Temp. & High Humidity Storage              | 60 °C, 85% RH, 240 hrs                                              |                              |                                                                                                                                                          |
| Vibration Test<br>(Non-operating)               | Freq.:10~55~10 Hz, Amp.:1.5mm<br>1 hr for each direction of X, Y, Z |                              |                                                                                                                                                          |
| Electrostatic Discharge Test<br>(Non-operating) | Terminals                                                           | 150 pF, 0 Ω, ±300 V, Contact |                                                                                                                                                          |
|                                                 | Panel                                                               | 150 pF, 330 Ω, ±8 KV, Air    |                                                                                                                                                          |
| Thermal Shock<br>(Static)                       | -30°C, 30 min /80°C, 30 min, 20 cycles                              |                              |                                                                                                                                                          |
| High Temperature Operation                      | 60 °C, 240 hrs                                                      |                              |                                                                                                                                                          |
| Low temperature Operation                       | -10 °C, 240 hrs                                                     |                              |                                                                                                                                                          |
| High Temperature & High Humidity<br>(Operating) | 50 °C, 90% RH, 240 hrs                                              |                              |                                                                                                                                                          |
| FPC Peeling Strength Test                       | Pull speed: 50 mm/min, +90°,                                        |                              | > 400gf/cm                                                                                                                                               |

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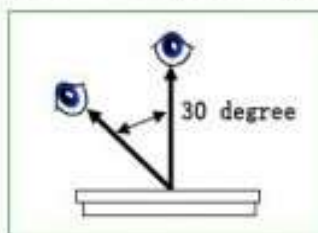
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## 10 Inspection Standard

This standard apply to TFT module specification.

### 1. Inspection condition:

Under daylight lamp 20~40W, product distance inspector'eye 30cm.incline degree 30° .



### 2. Inspection standard

| NO.                     | Item                 | Inspection standard                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | Rate              |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
|-------------------------|----------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------|-------------------|-------------------|--------|-------------------------|--------|-------------------------|----------------------|-------------------------|--------------|----------------------|---|-------|------------|--------------------------------|--|
| 2.1                     | Dot                  | <p>Case of Dot defect is below</p> <p>① Bright Dot (whit spot) : "0"</p> <p>② Dark Dot (black spot) : "0" (In case of Dark Dot on Main TFT LCD)</p> <p>- NG if there's full Dot defect.</p> <p>- Damaged less than the size of sub-pixel is not counted as defect</p> <p>- Dots darker than the size of sub-pixel are not defined as bright dot defect</p> <table><tr><th>area<br/>size (mm)</th><th>Acceptable number</th></tr><tr><td><math>\Phi \leq 0.10</math></td><td>ignore</td></tr><tr><td><math>0.10 &lt; \Phi \leq 0.15</math></td><td>3</td></tr><tr><td><math>0.15 &lt; \Phi \leq 0.20</math></td><td>2</td></tr><tr><td><math>0.25 &lt; \Phi \leq 0.25</math></td><td>1</td></tr><tr><td><math>0.25 &lt; \Phi</math></td><td>0</td></tr></table> | area<br>size (mm) | Acceptable number | $\Phi \leq 0.10$  | ignore | $0.10 < \Phi \leq 0.15$ | 3      | $0.15 < \Phi \leq 0.20$ | 2                    | $0.25 < \Phi \leq 0.25$ | 1            | $0.25 < \Phi$        | 0 | minor |            |                                |  |
| area<br>size (mm)       | Acceptable number    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| $\Phi \leq 0.10$        | ignore               |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| $0.10 < \Phi \leq 0.15$ | 3                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| $0.15 < \Phi \leq 0.20$ | 2                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| $0.25 < \Phi \leq 0.25$ | 1                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| $0.25 < \Phi$           | 0                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| 2.2                     | line                 | <table><tr><th colspan="2">Size (mm)</th><th>Acceptable number</th></tr><tr><td>ignore</td><td><math>W \leq 0.03</math></td><td>ignore</td></tr><tr><td><math>L \leq 4.0</math></td><td><math>0.03 &lt; W \leq 0.04</math></td><td>2</td></tr><tr><td><math>L \leq 4.0</math></td><td><math>0.04 &lt; W \leq 0.05</math></td><td>1</td></tr><tr><td></td><td><math>0.05 &lt; W</math></td><td>Treat with dot non-conformance</td></tr></table>                                                                                                                                                                                                                                                                                                                 | Size (mm)         |                   | Acceptable number | ignore | $W \leq 0.03$           | ignore | $L \leq 4.0$            | $0.03 < W \leq 0.04$ | 2                       | $L \leq 4.0$ | $0.04 < W \leq 0.05$ | 1 |       | $0.05 < W$ | Treat with dot non-conformance |  |
| Size (mm)               |                      | Acceptable number                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| ignore                  | $W \leq 0.03$        | ignore                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| $L \leq 4.0$            | $0.03 < W \leq 0.04$ | 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
| $L \leq 4.0$            | $0.04 < W \leq 0.05$ | 1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |
|                         | $0.05 < W$           | Treat with dot non-conformance                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |                   |                   |                   |        |                         |        |                         |                      |                         |              |                      |   |       |            |                                |  |



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|  |                                       |
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|  | Carton outline size: 400×295×145 (mm) |
|--|---------------------------------------|

## 12 Precautions

Please pay attentions to the followings as using the LCD module.

### 12.1 Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the polarizer is very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.
- (e) Avoid using Ketone type materials (e.g. Acetone), Toluene, Ethyl acid or Methyl chloride to clean the display surface. It might damage the touch panel surface permanently. The recommended solvents are water and Isopropyl alcohol.
- (f) Wipe off water droplets or oil immediately.
- (g) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (h) Do not touch the output pins directly with bare hands.
- (i) Do not disassemble the LCD module.
- (j) Do not lift the FPC of Touch Panel.

### 12.2 Storage

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong

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ultraviolet ray for a long time.

- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

### 12.3Operation

- (a) When mounting or dismounting the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.
- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.
- (h) Most of the touch screens have air vent to equalize the inside air pressure to the outside one. The air vent must be open and liquid contact must be avoided as the liquid may be absorbed if the liquid is accumulated near the air vent.
- (i) For the fragility of ITO film, it should avoid to use too tapering pen as the input material.

### 12.4Touch Panel Mounting Notes

- (a) If a cushion is used between bezel/housing and film must be choose as free as enough to absorb the expansion and contraction to avoid the distortion of film.
- (b) The cushion must be placed out of the Viewing Area.
- (c) Bezel/Housing edge must be posited between Key Area and Viewing Area. The edge enters the Key Area may cause unexpected input if the gap is too narrow or foreign particles like dusts exist between Bezel/Housing and ITO film.



