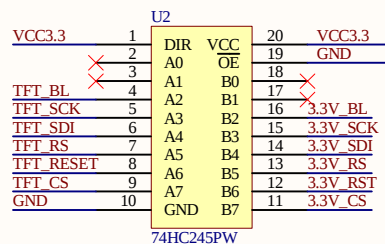
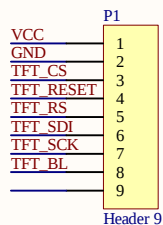


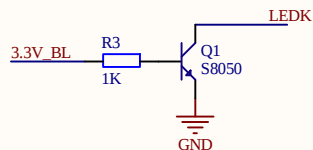
液晶屏接口电路



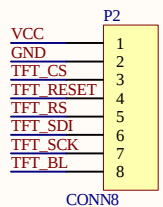
电平转换电路



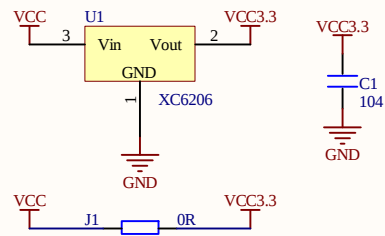
9PIN输出排针



背光控制电路（可IO或者PWM控制）
TFT_BL 高电平使能



8PIN输出排针



供电电源选择：

外部VCC输入5V时，J1断开
外部VCC输入3.3V时，J1短接

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